



DPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 1590**

Taketo HEISHI et al. : Docket No. 2003_1076A

Serial No. 10/630,705 : Group Art Unit 2186

Filed July 31, 2003 :

COMPILER, COMPILER APPARATUS
AND COMPILATION METHOD

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 CFR 1.56, 1.97 and 1.98, Applicants request consideration of the references listed on attached form PTO-1449 and any additional information identified below in paragraph 3. A legible copy of each reference listed on the Form PTO-1449 is enclosed, except a copy is not provided for:

- ☐ each U.S. Patent and U.S. Patent application publication;
- ☐ each reference previously cited in the international application PCT/_____;
and/or
- ☐ each reference previously cited in prior parent application Serial No.
_____.

1a. [X] This Information Disclosure Statement is submitted:

within three months of the filing date (or of entry into the National Stage) of the above-entitled application, **or**

before the mailing of a first Office Action on the merits or the mailing of a first Office Action after the filing of an RCE,

and thus no certification and/or fee is required.

1b. ☐ This Information Disclosure Statement is submitted

after the events of above paragraph 1a and prior to the mailing date of a final Office Action or a Notice of Allowance or an action which otherwise closes prosecution in the application, and thus:

(1) ☐ the certification of paragraph 2 below is provided, **or**

(2) ☐ the fee of \$180.00 specified in 37 CFR 1.17(p) is enclosed.

1c. ☐ This Information Disclosure Statement is submitted:

after the mailing date of a final Office Action or Notice of Allowance or action which otherwise closes prosecution in the application, and prior to payment of the issue fee, and thus:

the certification of paragraph 2 below is provided, and

the fee of \$180.00 specified in 37 CFR 1.17(p) is enclosed.

2. It is hereby certified

a. ☐ that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or

b. ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the Statement.

3. ☐ Consideration of the following list of additional information (including any copending or abandoned U.S. application, prior uses and/or sales, etc.) is requested.

4. For each non-English language reference listed on the attached form PTO-1449, reference is made to:
- a. ☐ a full or partial English language translation submitted herewith,
 - b. ☐ a foreign patent office search report (in the English language) submitted herewith,
 - c. ☐ the concise explanation contained in the specification of the present application at page,
 - d. ☐ the concise explanation set forth in the attached English language abstract,
 - e. ☐ the concise explanation set forth below or on a separate sheet attached to the reference:
5. ☒ A foreign patent office search report citing one or more of the references is enclosed.
6. ☐ Statement Under 37 CFR 1.704(d)

Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart application, and this communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the Information Disclosure Statement.

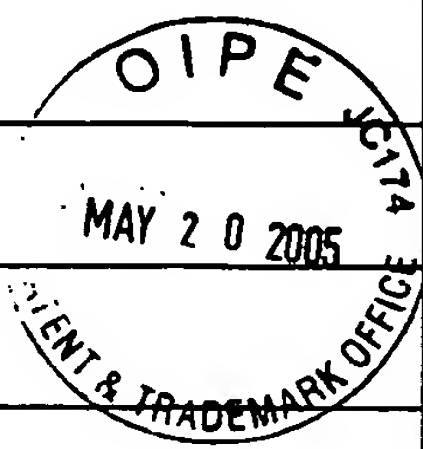
Respectfully submitted,

Taketo HEISHI et al.

By 

Michael S. Huppert
Registration No. 40,268
Attorney for Applicants

MSH/kjf
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
May 20, 2005

| | | | | | | | |
|---|----|---|-----------------------------------|-----------------|--------------------------|----------|---|
| Sheet 1 of 1 | | INFORMATION DISCLOSURE STATEMENT | | | | | |
| FORM PTO 1449 (modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) <i>(Use several sheets if necessary)</i> Date Submitted to PTO: May 20, 2005 | | | ATTY DOCKET NO. 2003_1076A | | SERIAL NO. 10/630,705 | |  |
| | | | APPLICANT Taketo HEISHI et al. | | | | |
| | | | FILING DATE July 31, 2003 | | GROUP 2186 | | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
| | AA | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES NO |
| | AB | 0 947 923 | 10/1999 | EP | | | |
| OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| | AC | TREMBLAY, M. et al., " <u>VIS SPEEDS NEW MEDIA PROCESSING</u> ", IEEE MICRO, IEEE INC. New York, US, vol. 1 no. 4, August 1, 1996; pages 10-20 (XP000596509). | | | | | |
| | AD | " <u>VIS INSTRUCTIONS SET USER'S MANUAL</u> ", May 2001, Sun Microsystems, Palo Alto (XP002307554). | | | | | |
| | AE | SHI, Hao et al., " <u>C/C++ COMPILER SUPPORT FOR SIEMENS TRICORE DSP INSTRUCTION SET</u> ", Acoustics, Speech, and Signal Processing, 1999. Proceedings., 1999 IEEE International Conference on Phoenix, AZ, USA, IEEE, US, March 15, 1999, pages 2147-2150 (XP010327807). | | | | | |
| | AF | BERTIN, V. et al., " <u>FLEXCC2: AN OPTIMIZING RETARGETABLE C COMPILER FOR DSP PROCESSORS</u> ", Embedded Software. Second International Conference, EMSOFT 2002, Proceedings (Lecture Notes in Computer Science vol. 2491) Springer-Verlag Berlin, Germany, October 2002, pages 382-398 (XP002307133). | | | | | |
| | AG | GLOSSNER, J., et al. " <u>TRENDS IN COMPILABLE DSP ARCHITECTURE</u> ", IEEE Workshop on Signal Processing Systems. SIPS 2000. Design and Implementation, October 11, 2000, pages 181-199 (XP010525228). | | | | | |
| | AH | WAGNER, J., et al. " <u>C COMPLIER DESIGN FOR AN INDUSTRIAL NETWORK PROCESSOR</u> ", Sigplan Notices ACM USA, vol. 36, no. 8, August 2001, pages 155-164 (XP002307134). | | | | | |
| | AI | BATTEN, D. et al., " <u>A NEW APPROACH TO DSP INTRINSIC FUNCTIONS</u> ", Proceedings of the 33rd Hawaii International Conference on System Sciences, January 4, 2000, pages 2892-2901 (XP010545579). | | | | | |
| | AJ | ROODZANT, M. " <u>REAL-TIME DSP APPLICATIONS BENEFIT FROM HIGH-LEVEL LANGUAGE COMPILATION</u> ", Real-Time Magazine Real-Time Consult, Belgium, April 1999, page 40, 54 (XP002307135). | | | | | |
| EXAMINER | | | | DATE CONSIDERED | | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of form with next communication to applicant.